

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4632	kito.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/26 07:40
S2	59	S1 and masaru	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/23 15:48
S3	274	aochi.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/23 16:15
S4	43	S3 and hideaki	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/23 16:19
S5	2	("6689646").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/23 16:20
S6	8	((("5200354") or ("20040129965") or ("5805494") or ("5998821"))). PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/23 16:22
S9	2	("6777294").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/26 07:40
S10	702	(257/300).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/27 16:41
S11	1232	(257/301).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/28 13:11
S12	1184	(memory and transistor) with (trench adj capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/28 13:13
S13	0	(memory and transistor) with ((trench adj capacitor) with (insulat\$4 adj on adj one adj side) with contact)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/28 13:14
S14	56	(memory and transistor) with ((trench adj capacitor) with (insulat\$4) with contact)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/28 13:27

S15	1	(memory and transistor) with ((trench adj capacitor) with (insulat\$4) with conduct\$3 with (gate adj electrode) with contact)	US-PGPUB	OR	ON	2005/09/28 13:32
S16	308	(257/311).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/28 14:03
S17	3	("6713810").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/28 14:07